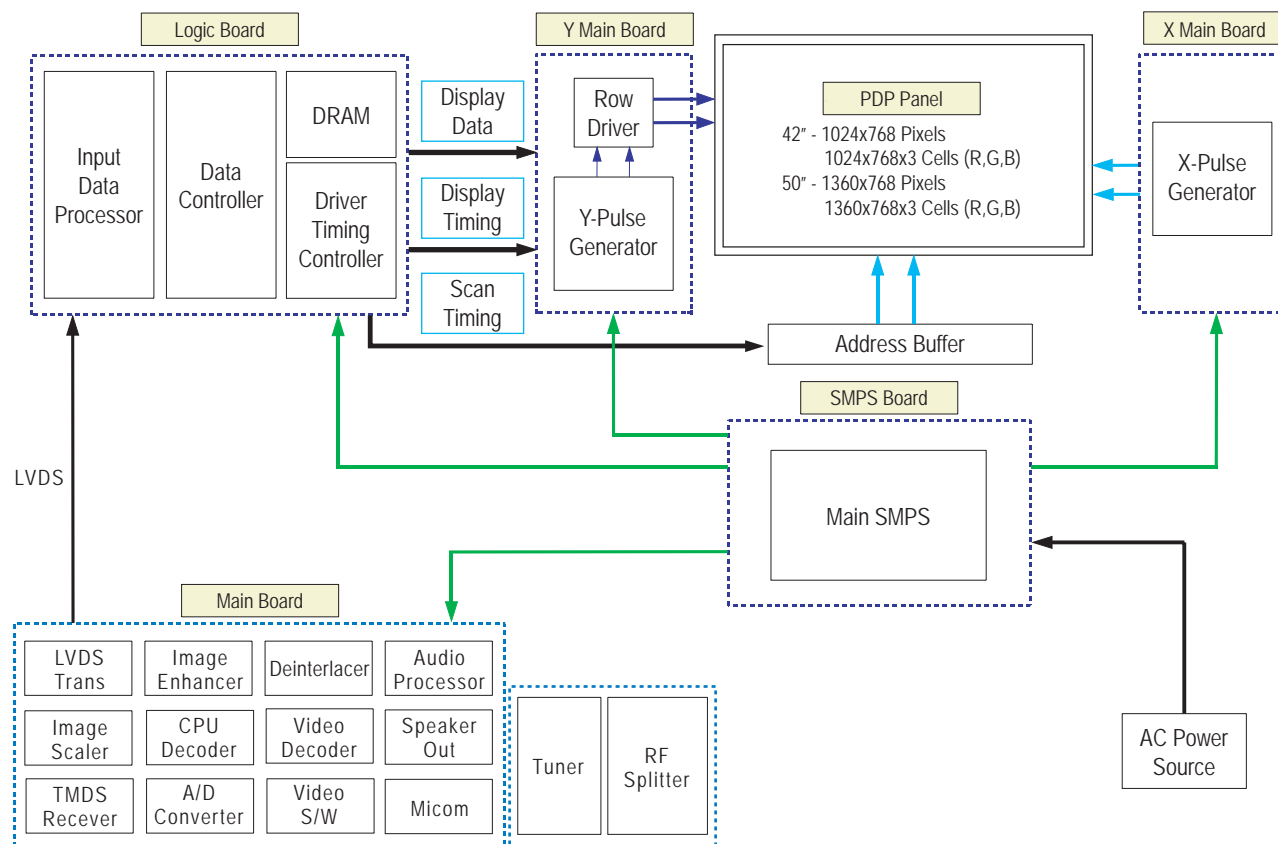


7. Schematic Diagram

7-1 Circuit Description



■ SMPS Board

The SMPS used for the PDP has been designed to be efficient, compact and lightweight. For VS and VA outputs, a LLC converter has been used. For the other outputs, a Flyback converter has been used.

■ LOGIC Board

The logic circuit consists of a Logic Main Board and an Address Buffer Board. The Logic Main Board decodes the video signal encoded by the Video Board, outputs the ADDRESS data signal for each pattern and generates X and Y drive signals. The Address Buffer Board buffers and transfers the ADDRESS data output signal using TCP IC.

- LVDS with built-in video signal processing (W/L, error diffusion, APC, FCR, etc.) applied and 1 ASIC chip.
- Outputs the address Drive IC control and data signals to the Buffer Board.
- Outputs the control signal for the X and Y Drive Boards.
- Monitors major drive voltages (Micom Circuit Block); detects if a surge voltage has been applied and protects the Drive Circuit.
- Temperature Adaptive Operating Mode (Low Temperature/Room Temperature/High Temperature); Discharge optimization for each temperature level.

■ X-MAIN Board

Connects to the X terminal block, 1) provides maintaining voltage waveform (including ERC), and 2) maintains the Ve bias in the Scan section.

■ Y-MAIN Board

Connects to the Y terminal block, 1) provides maintaining voltage waveform (including ERC), 2) provides Y Rising, Falling Ramp waveforms, and 3) maintains the Vscan bias.

■ Address Buffer Board




It delivers the data signal and control signal to the TCP.

MEMO

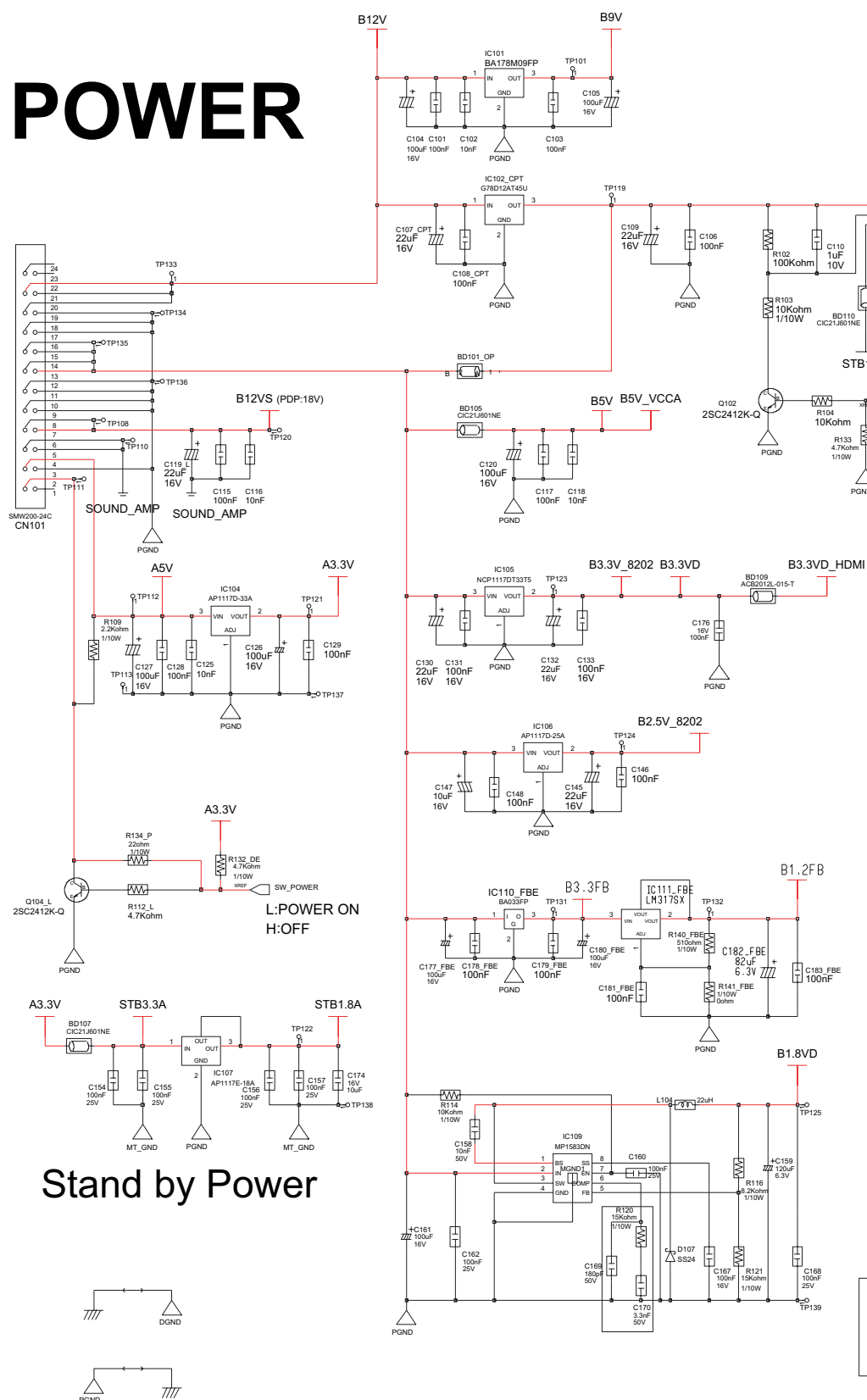
7-2 Schematic Diagram

7-2-1 POWER_ETC

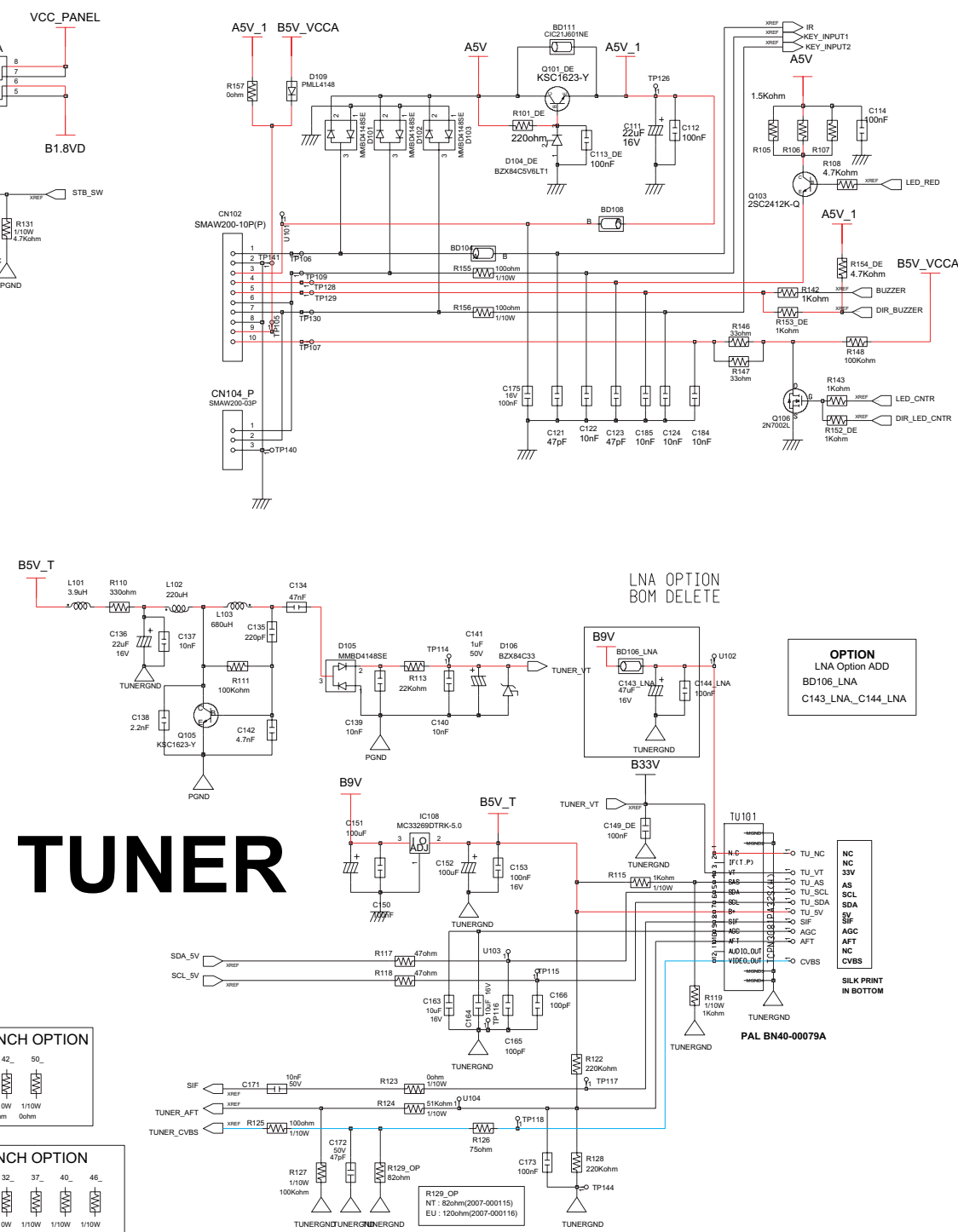
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	Power
	Vidoe
	Audio




POWER

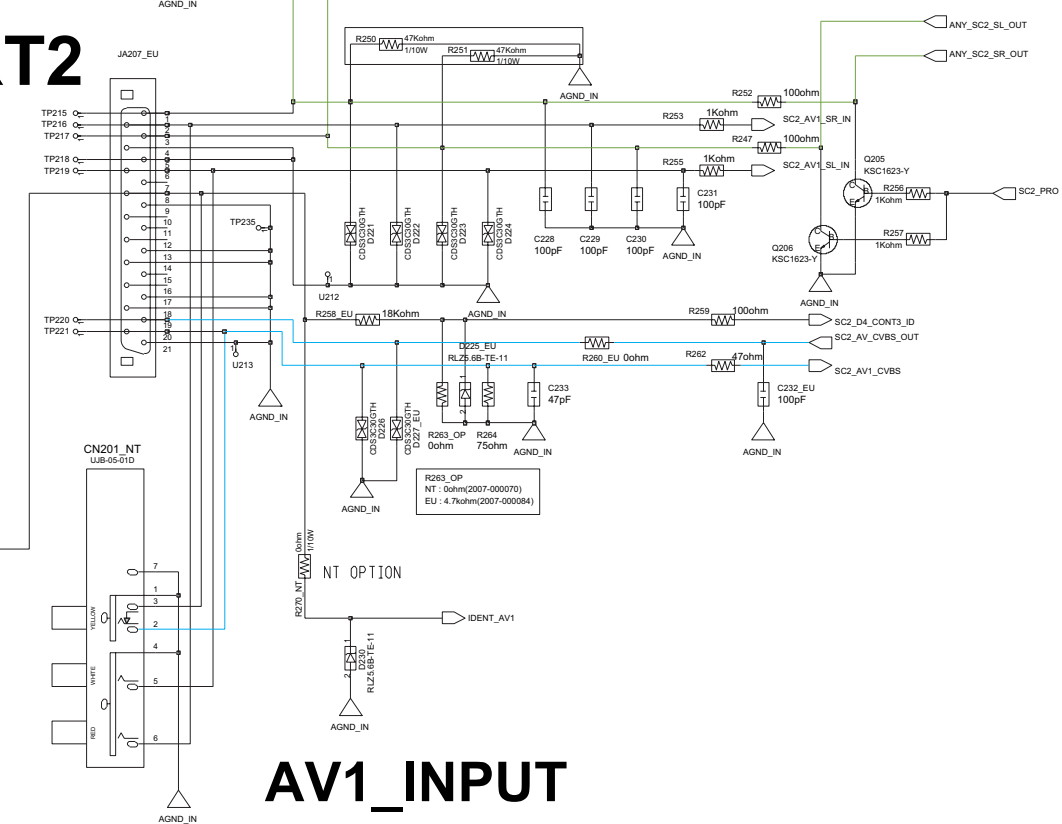
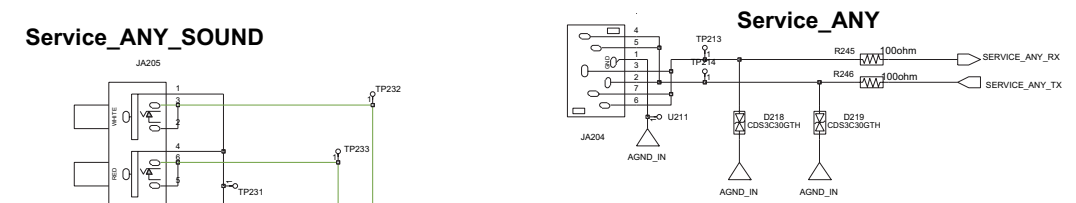
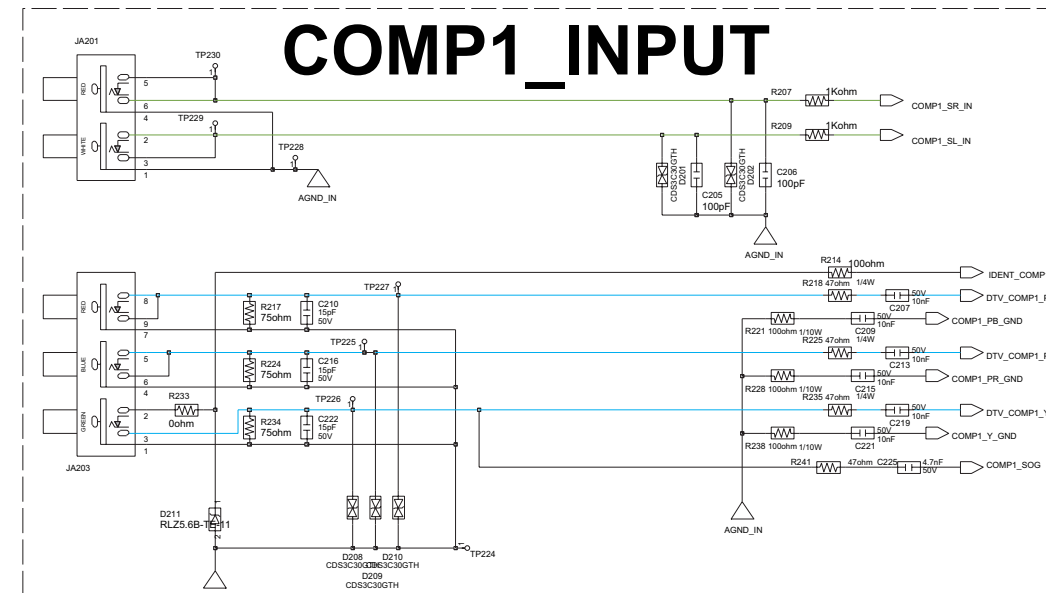


Function



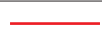


TUNER

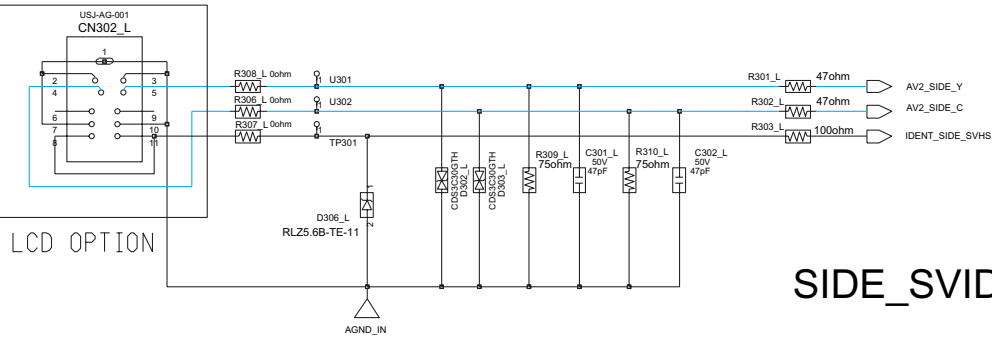
	Power
	Vidoe
	Audio



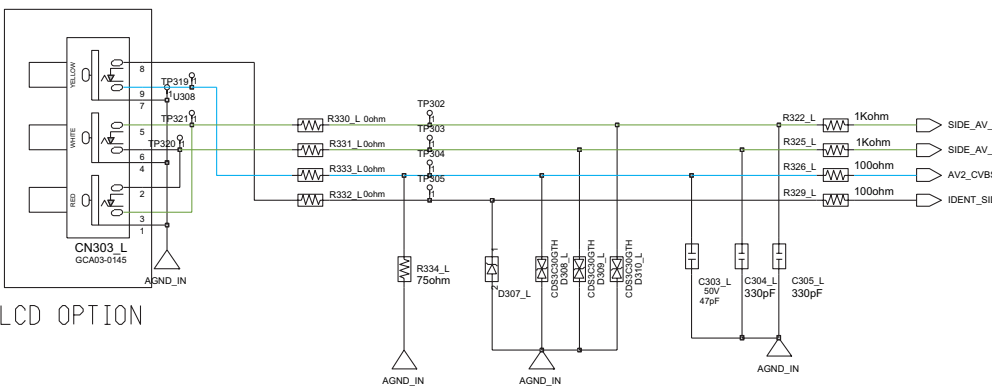
7-2-3 INPUT_JACK2

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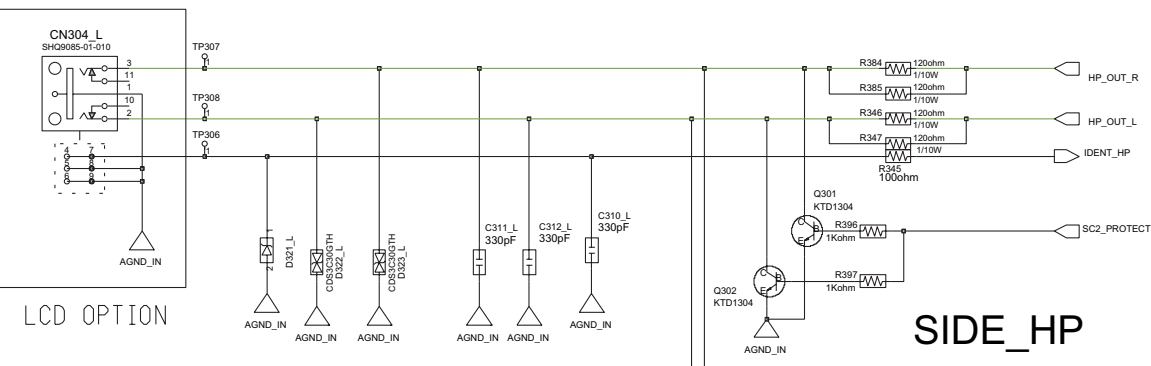
	Power
	Vidoe
	Audio



SIDE_SVIDEO INPUT



SIDE_AV INPUT

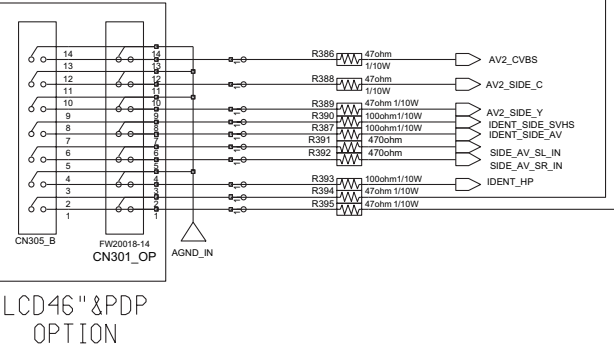


SIDE_HP

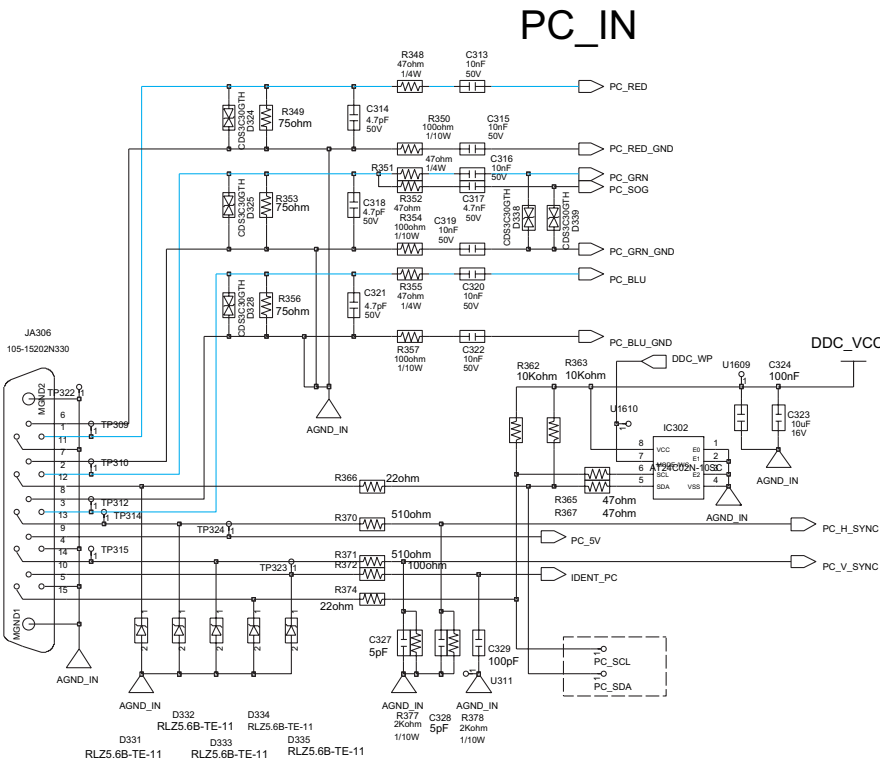
SIDE_AV

	JA202_EU	JA207_EU	JA206_NT	CN201_NT	JA208_JP JA209_JP
PAL	O	O			
NT			O	O	
Japan					O

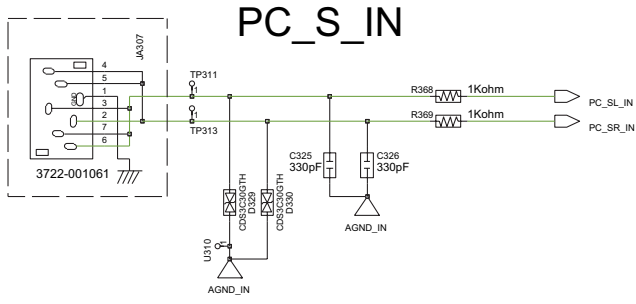
	CN301_OP	CN302_L	CN303_L	CN304_L
LCD		O	O	O
LCD46"&PDP	O			



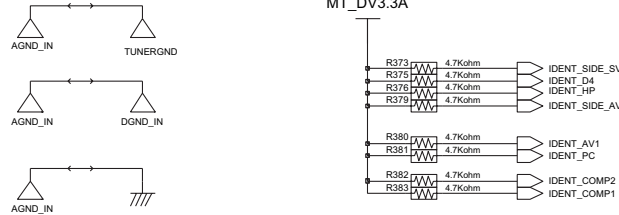
LCD46" & PDP
OPTION






PC_IN

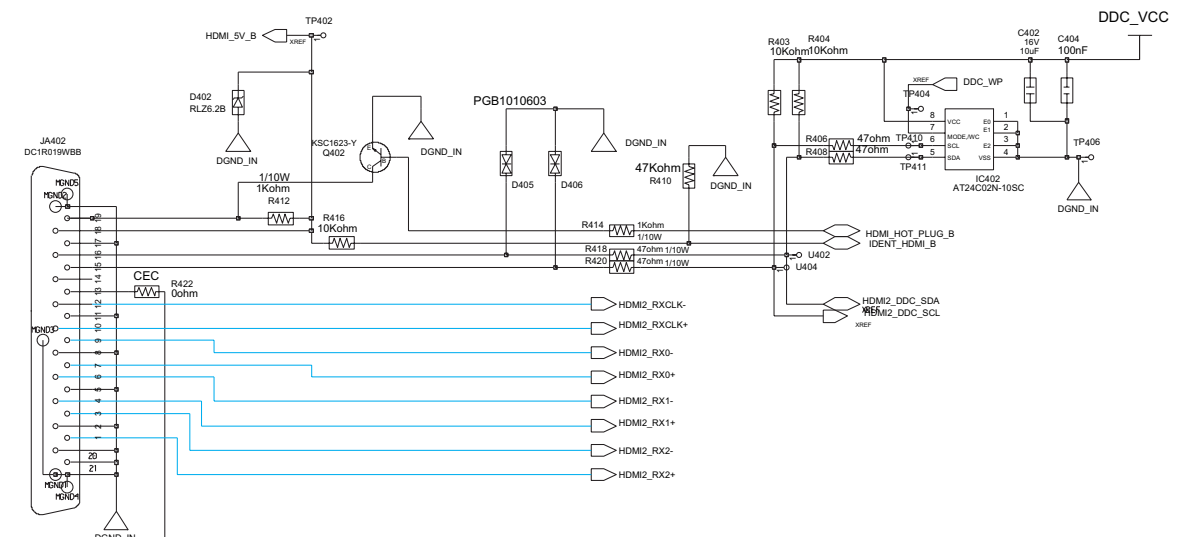


PC_S_IN

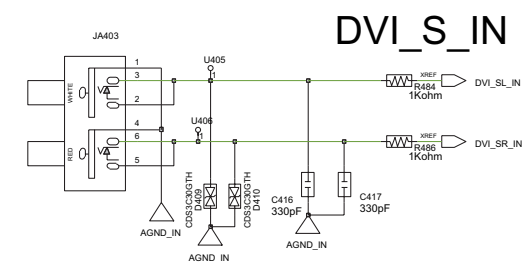


	Power
	Vidoe
	Audio

HDMI2_INPUT



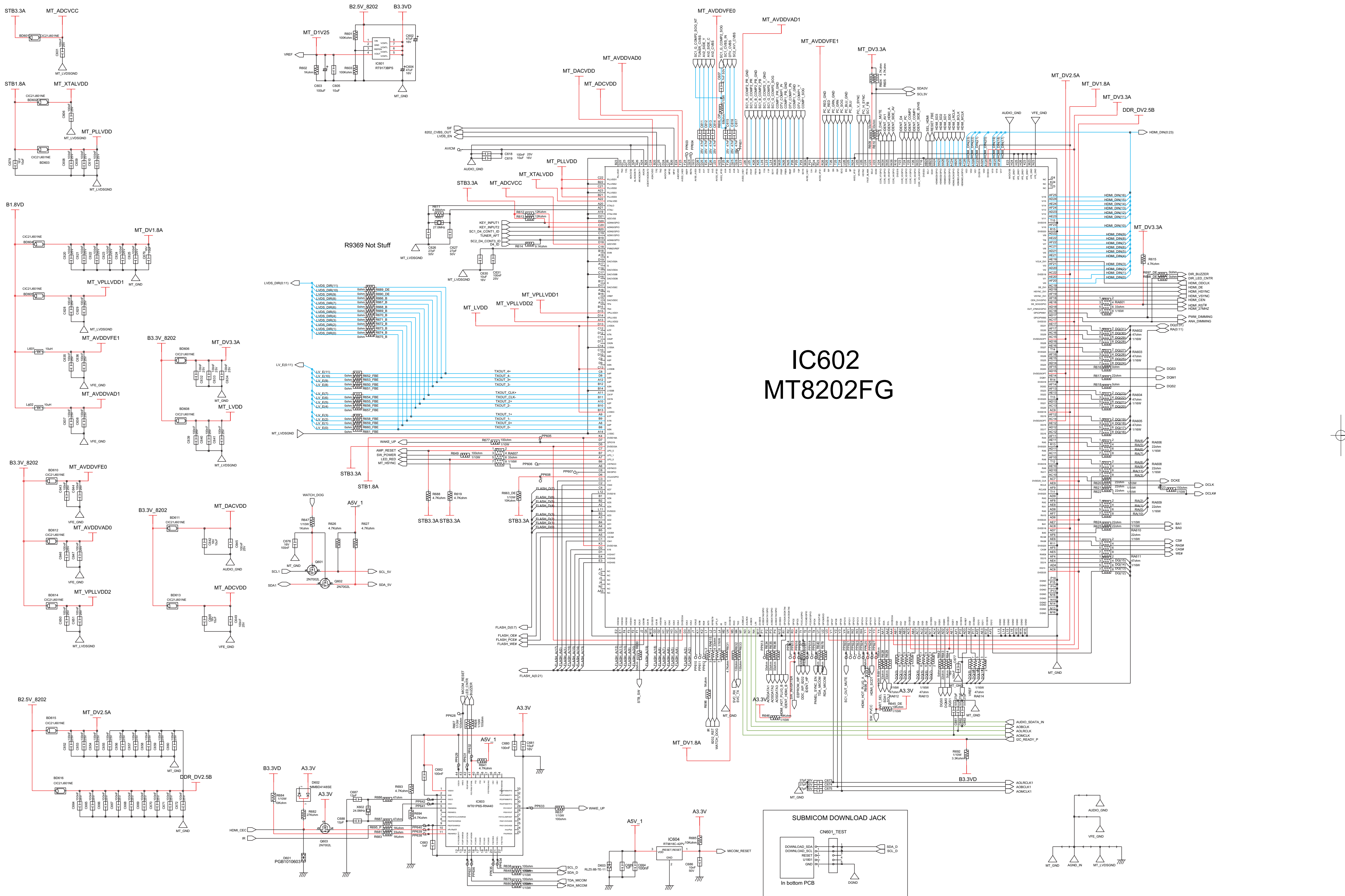
SEL_HDMI	HIGH	LOW
OUTPUT	HDMI2	HDMI1



7-2-6 MT8202



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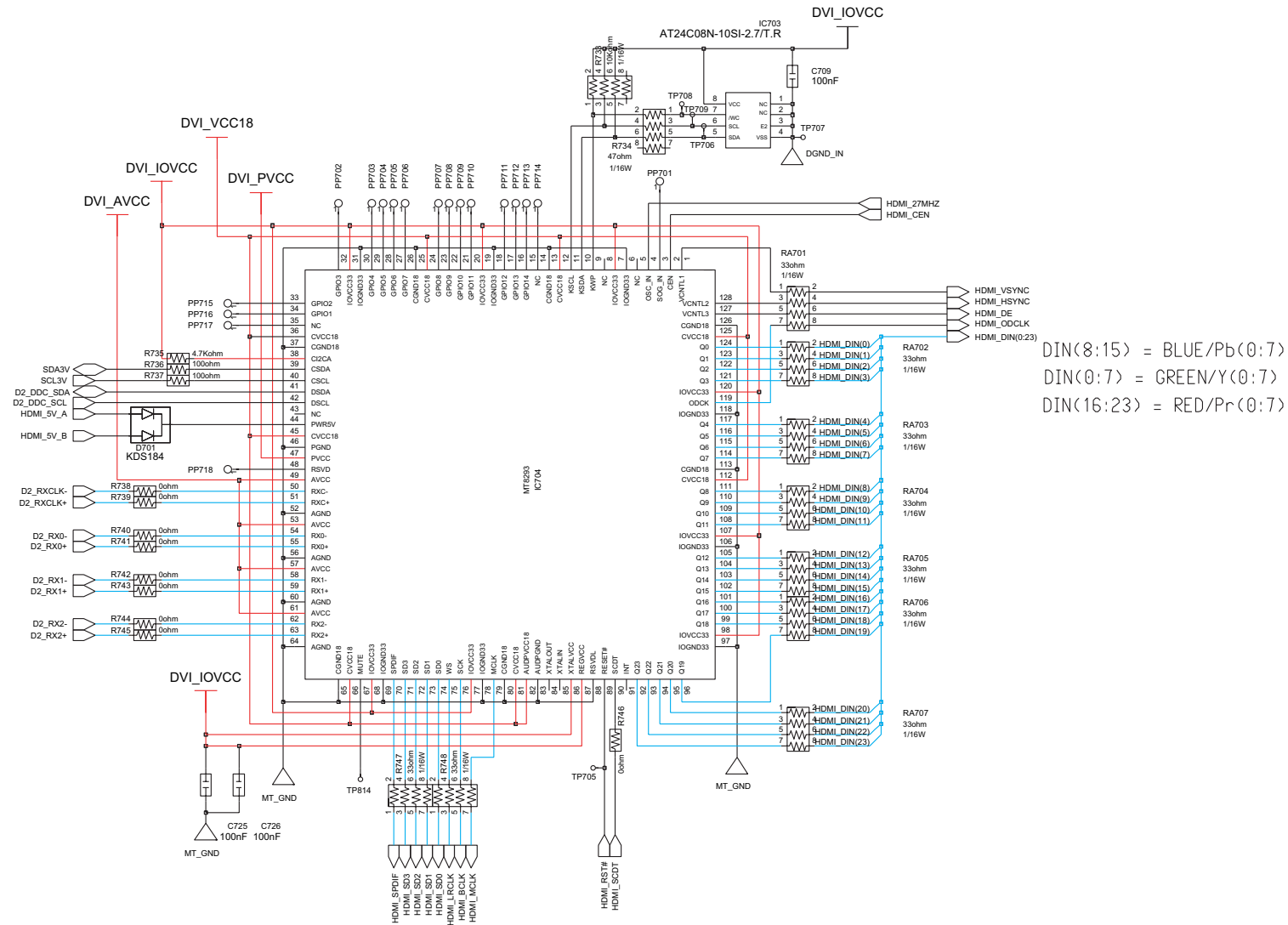
—	Power
—	Videoe
—	Audio



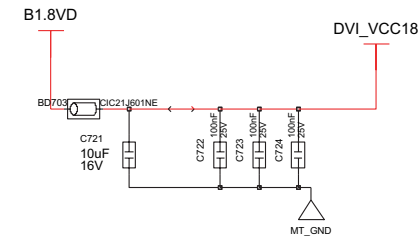
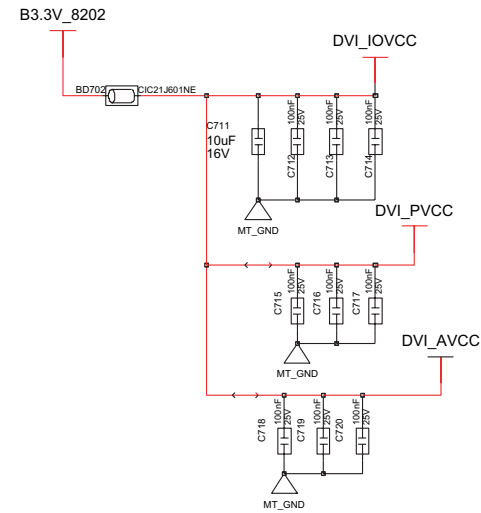
7-2-7 HDMI_MT8293




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	Power
	Vidoe
	Audio



Near to Connector





	Power
	Vidoe
	Audio

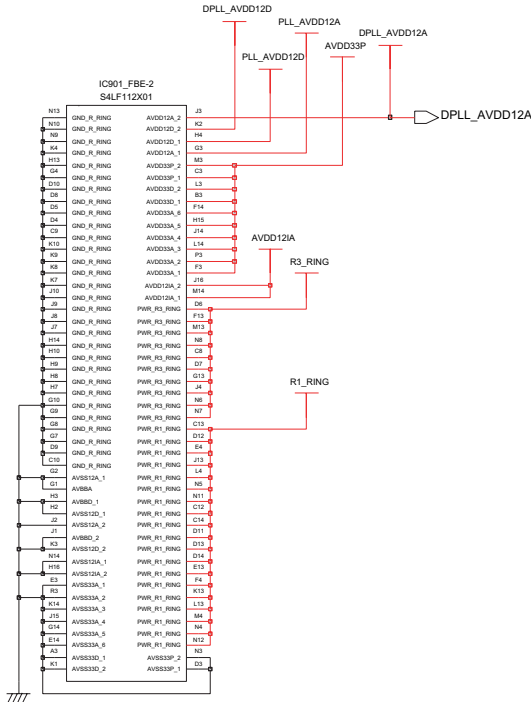
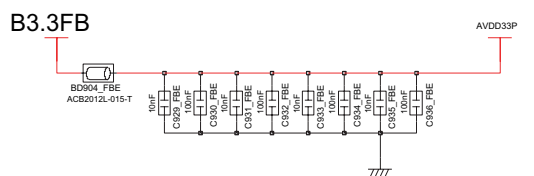
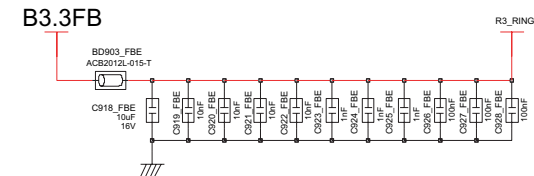
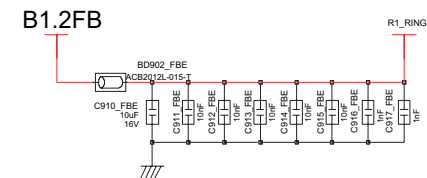
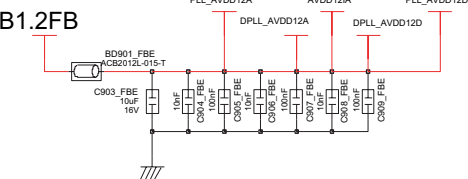
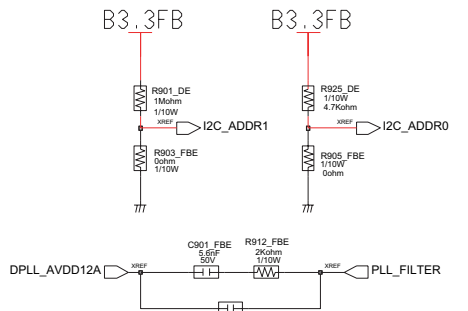
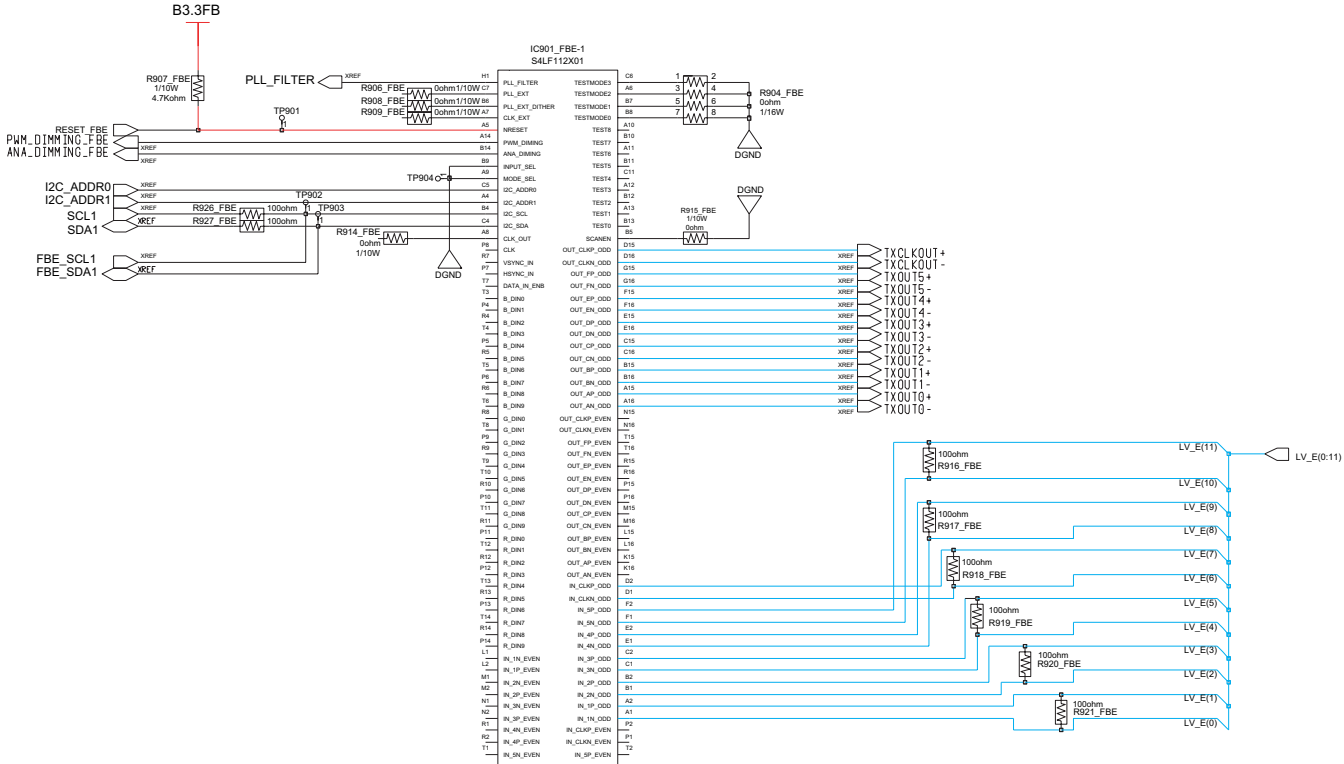
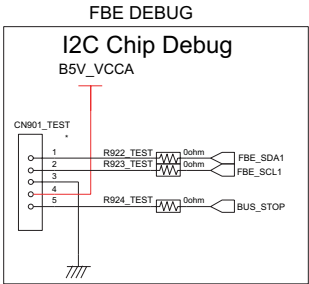


7-2-9 FBE_LVDS

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	Power
	Vidoe
	Audio

F B E 2

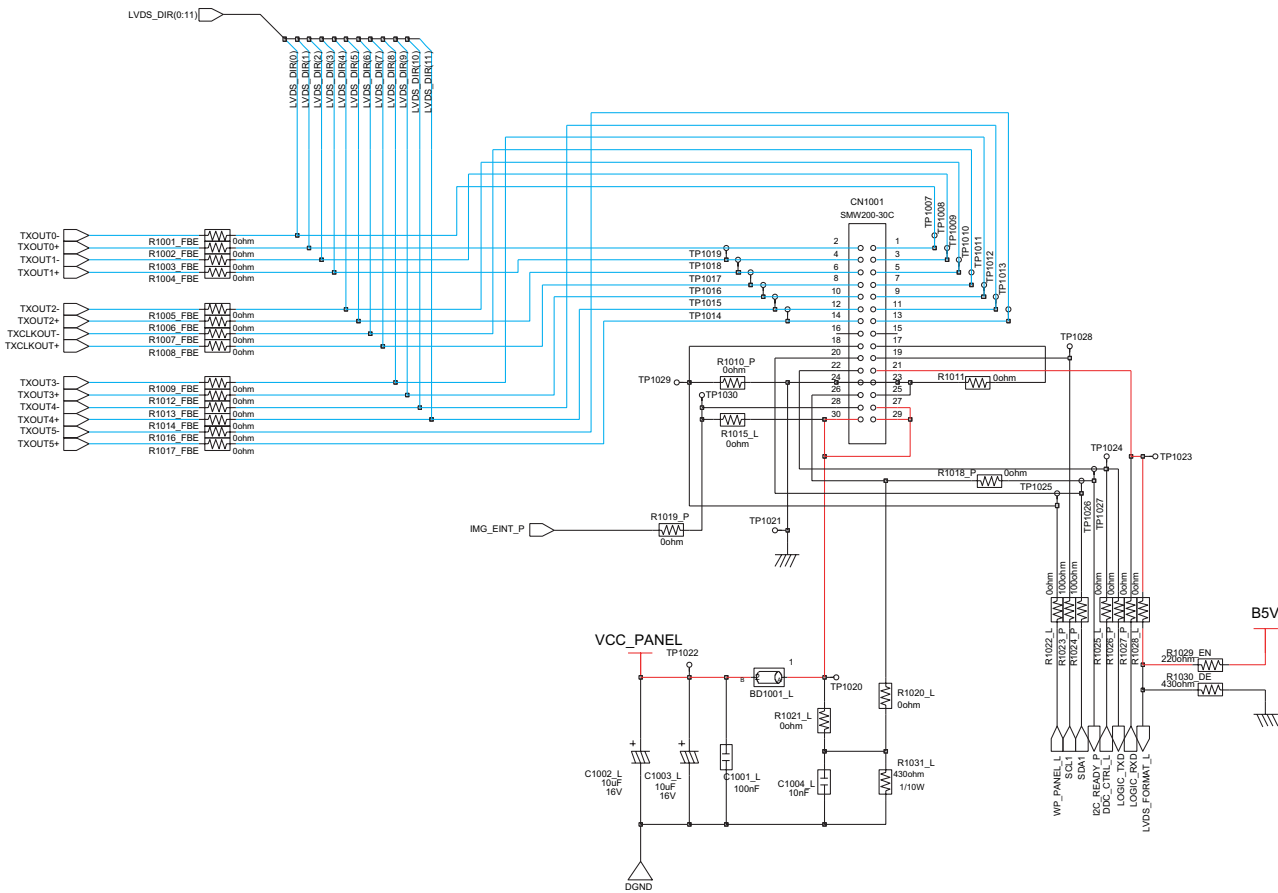


7-2-10 LVDS_TX_LBE

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<div></div>	Power
<div></div>	Vidoe
<div></div>	Audio

LVDS&Dimming



LCD OPTION

